

How to Simulate RF Materials Considering Accuracy and Speed

Speaker: Dr. Tracey Vincent - CST, Framingham, MA

Mail: Tracey.Vincent@cst.com

Abstract

Microwave engineers and signal integrity engineers designing high speed electronics need to accurately simulate materials for accurate results, particularly losses in their designs. Full-wave solvers offers sophisticated characterizing and parameterizing of materials for accurate simulation that accounts for both conductor and dielectric losses. This presentation will show features and strategies, and the theory behind them, that can help to create a straightforward workflow for fast and accurate results. A case study of a copper foil will show how multi-faceted this problem is and the many ways to approach it, including the ERD method which is a novel way of capturing surface roughness impact on insertion loss.